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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/656,352	09/05/2003	Swee Kwang Chua	2269-5377.1US (02-0388.01	7824
24247 7.	590 03/12/2004		EXAM	INER
TRASK BRITT			CLARK, JASMINE JHIHAN B	
P.O. BOX 2550)			
SALT LAKE CITY, UT 84110			ART UNIT	PAPER NUMBER
			2815	

DATE MAILED: 03/12/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/656,352	CHUA ET AL.	\$\$				
Office Action Summary	Examiner	Art Unit					
	Jasmine Clark	2815					
The MAILING DATE of this communication Period for Reply	appears on the cover sh	eet with the correspondence a	ddress				
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If the period for reply specified above is less than thirty (30) days, and if NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by some Any reply received by the Office later than three months after the meaned patent term adjustment. See 37 CFR 1.704(b).	DN. R 1.136(a). In no event, however, n. a reply within the statutory minimur priod will apply and will expire SIX (tatute, cause the application to be	may a reply be timely filed n of thirty (30) days will be considered time 6) MONTHS from the mailing date of this of ome ABANDONED (35 U.S.C. § 133).	ely. communication.				
Status							
1) Responsive to communication(s) filed on _							
2a) ☐ This action is FINAL . 2b) ☑ 2	This action is non-final.						
·— · · ·	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) ☐ Claim(s) 1-26 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) 13-26 is/are allowed. 6) ☐ Claim(s) 1 is/are rejected. 7) ☐ Claim(s) 2-12 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11)☐ The oath or declaration is objected to by th							
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
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Attachment(s)	_	11	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,				
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948 Information Disclosure Statement(s) (PTO-1449 or PTO/St Paper No(s)/Mail Date) Par	rview Summary (PTO-413) er No(s)/Mail Date ice of Informal Patent Application (PT er:	O-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Akram (US 5,936,305).

Akram teaches a device comprising a plurality of semiconductor dies 106, each semiconductor die having a back surface, and active surface and a plurality of signal connection devices (bond pad 110) on the active surface; a substrate 102 having a top surface, a bottom surface and a plurality of cavities formed in the top surface of the substrate 102, wherein each of the plurality of signal connection devices is positioned in one of the plurality of cavities and the active surface of the plurality of the semiconductor dies 106 are adhered to the top surface of the substrate 102, and wherein a plurality of openings is formed in the bottom surface of the substrate exposing the plurality of signal connection devices 110 therethrough; and a molding layer (see Fig. 5) over the top surface of the substrate and the back surfaces of the plurality of semiconductor dies 106.

Allowable Subject Matter

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2. Claims 2-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The applied reference fails to teach including a first dielectric layer upon the bottom surface of the substrate; and a plurality of openings in the first dielectric layer exposing the plurality of signal connection devices therethrough.

3. Claims 13-26 are allowed.

The following is an examiner's statement of reasons for allowance: the applied reference fails to teach and/or suggest the limitations as set forth in claims 13-15, 25, and 26.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

The references Cited

4. Akram et al. (US 6,617,687 B2) show a plurality of connection devices in a plurality of cavities. Goetz et al. (US 6,175,161 B1) show a plurality of signal, power and ground layers, eg, 12a-12d within a substrate with connection signal devices, eg, bumps connected therefrom. Nishimori (JP 63-69258) show a plurality of semiconductor dies and timing interconnection layer in a substrate.

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Telephone Inquiry Contacts

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasmine Clark whose telephone number is (571) 272-1726. The examiner can normally be reached on M-F from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas, can be reached on (571) 271-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jjbc/03/03/04

JASMINE CLARK
PRIMARY EXAMINER